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**Date:** November 23, 2004

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**Message:**

U.S. Pat. Appl. Serial No. 10/017,426

Filed: December 18, 2001

Inventor(s): Ju Chun YEO et al.

Title: Liquid Crystal Display Device and Method for Fabricating the Same

Enclosed please find a document entitled "Discussion material" for the interview tomorrow, November 23, 2004. This document is only for discussions and not for entry.

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**NOT FOR ENTRY**

In re Patent Application of:  
Ju-Chun Yeo et al.

Confirmation No.: 6363

Application No.: 10/017,426

Art Unit: 2673

Filed: December 18, 2001

Examiner: M. M. Said

For: LIQUID CRYSTAL DISPLAY DEVICE AND  
METHOD FOR FABRICATING THE SAME

Customer No.: 30827

**DISCUSSION MATERIAL (NOT FOR ENTRY)**

**Claim 1:** A liquid crystal display device having an interconnection line part for applying a signal from a driving circuit to a liquid crystal display comprising:

a substrate; and

a plurality of interconnection lines on the substrate, the interconnection lines are wider at a center portion of the interconnection line part than at an outer portion of the interconnection line part.

(1) Fujii (USP 5,757,450) fails to teach or suggest "a plurality of interconnection lines on the substrate, the interconnection lines are wider at a center portion of the interconnection line part than at an outer portion of the interconnection line part."

(2) Fig. 2 in Fujii et al. discloses that w2 (the width of 42-2) is greater than w1 (the width of 42-1), and that 42-1 is closer to the center line 44 than 42-2. Accordingly, Applicants respectfully submit that Fujii et al. actually teaches away from the teachings of the present application.

**Claim 4:** A liquid crystal display device comprising:

a plurality of interconnection lines for applying a signal from a driving circuit to a liquid crystal display panel; and

a plurality of supplementary conductive patterns between the interconnection lines and connected with respective ones of interconnection lines.

(1) Hayakawa (USP 6,172,732) fails to teach or suggest "a plurality of supplementary conductive patterns between the interconnection lines and connected with respective ones of interconnection lines."

(2) Fig. 1 of Hayakawa fails to teach the element or limitation cited above.

**Claim 7:** A liquid crystal display device having an interconnection line part for applying a signal from a driving circuit to a liquid crystal display panel, comprising:

a substrate;

a conductive layer on the substrate, the conductive layer being wider at a center portion of the interconnection line part than at an outer portion of the interconnection line part and the conductive layer being electrically connected to a voltage line;

an insulating film on the conductive layer; and

a plurality of interconnection lines on the insulating film, the interconnection lines overlapping the conductive layer.

(1) Hayakawa (USP 6,172,732) fails to teach or suggest "a conductive layer on the substrate, the conductive layer being wider at a center portion of the interconnection line part than at an outer portion of the interconnection line part and the conductive layer being electrically connected to a voltage line"

(2) Hayakawa fails to teach the element or limitation cited above.

**Claim 20:** A liquid crystal display device having an interconnection line part for applying a signal from a driving circuit to a liquid crystal display panel, comprising:

a substrate;

a plurality of interconnection lines on the substrate;

an insulating film on the plurality of interconnection lines; and

a conductive layer on the insulating film, the conductive layer having a larger area at a center portion of the interconnection line part than at an outer portion of the interconnection line part.

(1) Hayakawa (USP 6,172,732) fails to teach or suggest "a conductive layer on the insulating film, the conductive layer having a larger area at a center portion of the interconnection line part than at an outer portion of the interconnection line part."

(2) Hayakawa fails to teach the element or limitation cited above.